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Navy Case No. 84,537
Application No. 10/774,643AMENDMENTS TO THE CLAIMS

1. (Currently Amended) In an electronic surveillance system having a receiver for receiving radio frequency pulse signals and for converting said radio frequency pulse signals into digitized radio frequency pulse descriptor word data and a presorter connected down-circuit of said receiver for further processing of digitized radio frequency pulse descriptor word data forwarded from the receiver, a pulse descriptor word (PDW) collector, comprising:

~~a pulse descriptor word (PDW) collector including~~ memory storage and a PDW extractor circuit for collecting all digitized radio frequency pulse descriptor word data received by said electronic surveillance system wherein said PDW extractor circuit is integrated into said electronic surveillance system at a point located between said receiver and said presorter; said PDW extractor circuit capturing said data and storing it into random access memory (RAM) while simultaneously allowing said data to flow unaltered and uninterrupted through said extractor to said presorter.

2. (Previously PCurrently Amended) ~~In an electronic surveillance system~~ A pulse descriptor word collector as in eClaim 1, wherein said RAM transfers said data to said memory storage for later retrieval.

3. (Original) A pulse descriptor word collector as in Claim 1, further comprising a personal computer coupled to said PDW extractor circuit, said personal computer

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including sequencing software to control the collecting of digitized radio frequency pulse descriptor word.

4. (Original) A pulse descriptor word collector as in Claim 1, wherein said electronic surveillance system is of a type having threat identification capabilities and said data is stored long-term in said personal computer so that said data may be played back for monitoring and assessment .

5. (Original) A pulse descriptor word collector as in Claim 2, wherein said collector is portable and may be removed to a laboratory setting for said play back and analysis.

6. (Original) A pulse descriptor word collector as in Claim 3, wherein said personal computer further comprises graphical user interface software giving the user point and click control to initiate and manage data collection.

7. (Previously Amended) A pulse descriptor word collector as in Claim 1, wherein said memory storage is comprised of Redundant Array of Independent Disks (RAID) hard drives.

8. (Original) A pulse descriptor word collector as in Claim 1, wherein said extractor further comprises:

- (a) input/output connections for communication with said receiver, said presorter and said personal computer,

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(b) timing alignment means and data flow control time coincidence for synchronizing the timing of data collection within said extractor with that of said electronic surveillance system;

(c) pulse descriptor word buffer for holding incoming pulse descriptor word data captured by said extractor from said electronic surveillance system until said incoming data is stable and ready to be stored by said extractor; said buffer using a first in first out control logic; said buffer also comprising a strobe for indicating that incoming data is ready to be stored;

(d) a random access memory (RAM) control connected to two RAM circuits; said RAM control and RAM circuits in communication with both said personal computer and said buffer; said RAM control controlled by said multi-state sequencing software of said personal computer and comprising a strobe monitor, a read/write select and a read/write control; said two RAM circuits for storing said data until it is ready to be stored by said personal computer; said two RAM circuits complimentary to each other such that when one is in a write mode, the other is in a read mode; said strobe monitor for monitoring said strobe from said buffer; said read/write control controlling a process of writing data from the buffer on one RAM while reading from the other RAM to the personal computer; said read/write select switching said RAM circuits back and forth between read and write modes; said two RAM circuits each further comprising a read counter and a write counter for monitoring the quantity of data that is being written to or read from said RAM circuits.

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9. (Original) A pulse descriptor word collector as in Claim 8, wherein said multi-state sequencing software is programmed with an interrupt; said interrupt programmed to cause the read/write select within the RAM control to switch said RAM circuits from write to read and vice versa at a predetermined interval.

10. (Original) A pulse descriptor word collector as in Claim 1, for use as a referee receiver for independently verifying signals received by the host electronic surveillance system.

11. (Previously Amended) In an electronic surveillance system having a receiver connected to a presorter, wherein the receiver receives radio frequency pulse signals, converts the said radio frequency pulse signals into digitized radio frequency pulse descriptor word data and then forwards said digitized radio frequency pulse descriptor word data to the presorter for further processing; a method for passively collecting all digitized radio frequency pulse descriptor word data while simultaneously allowing said data to flow unaltered and uninterrupted through said extractor to said presorter, said method comprising:

- (a) capturing all of said pulse descriptor word data forwarded from said receiver to said presorter;
- (b) simultaneously storing said data and allowing said data to flow unaltered and uninterrupted from said receiver through to said presorter;
- (c) continuously storing said data by establishing a read/write loop using two RAM circuits and a personal computer; said RAM circuits being complimentary to each

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other such that when one RAM circuit is in a read mode, the other RAM circuit is in a write mode and vice versa; said read/write loop is established when captured data is written onto a RAM in the write mode, while data, previously written onto the RAM that is currently in the read mode, is read to said computer and stored into long-term digital memory, then at a predetermined interrupt the RAM circuits switch modes; said read/write loop ensuring that none of the captured data is lost.

12. (Original) The method of Claim 11, further comprising: analyzing said collected data in a laboratory setting to monitor, assess and update the threat identification capabilities of said electronic surveillance system.